

Fig. 1
(Prior Art)

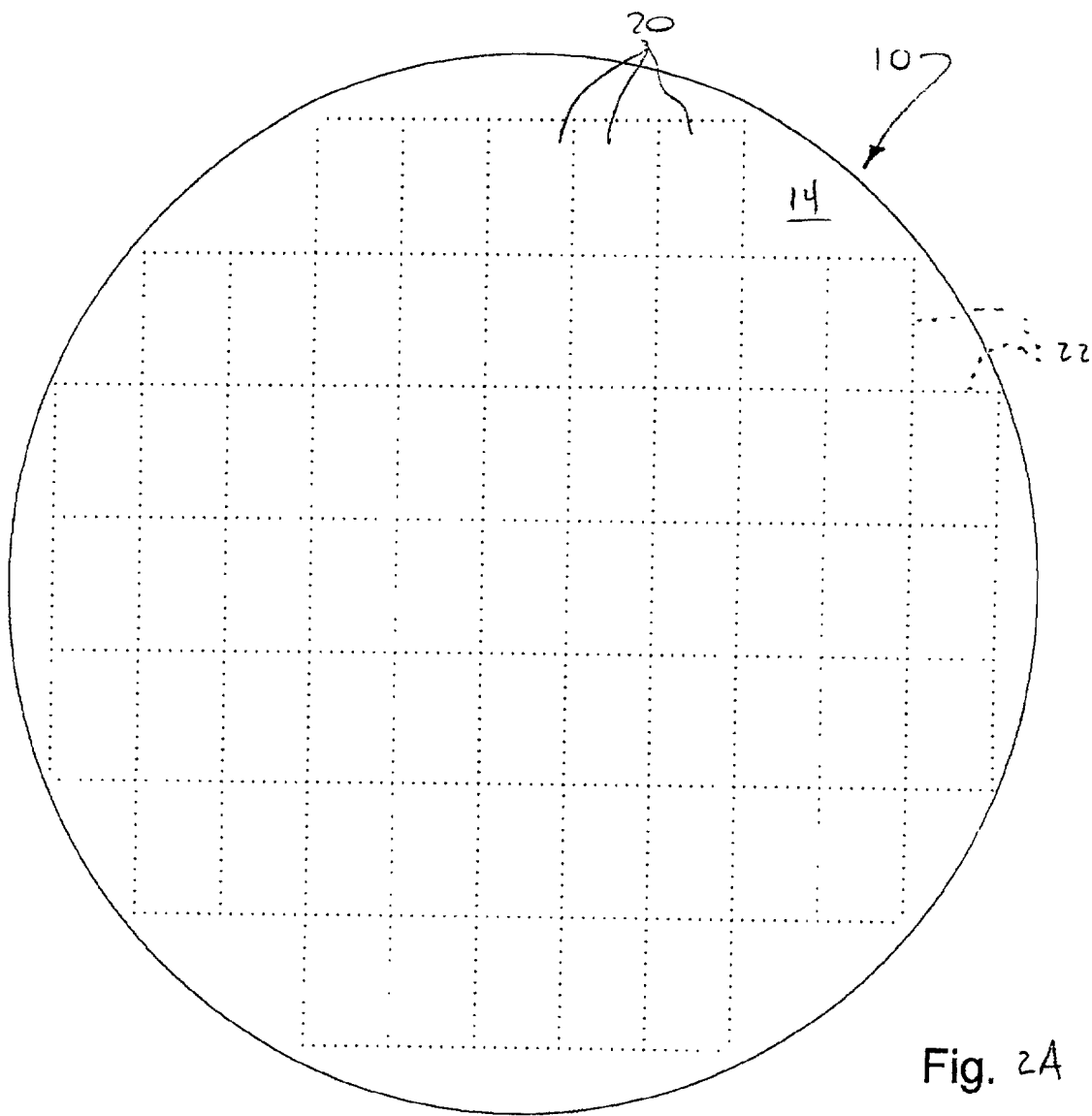


Fig. 2A

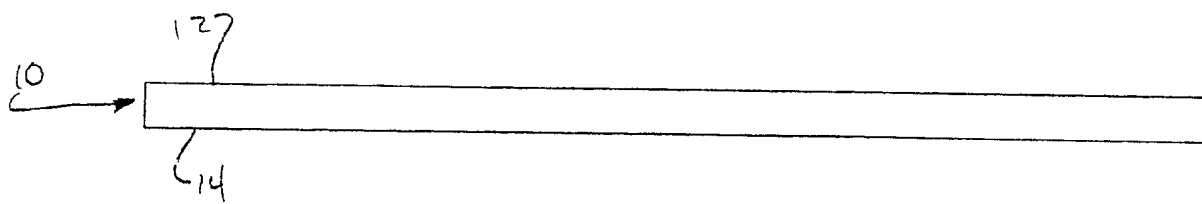


Fig. 2B

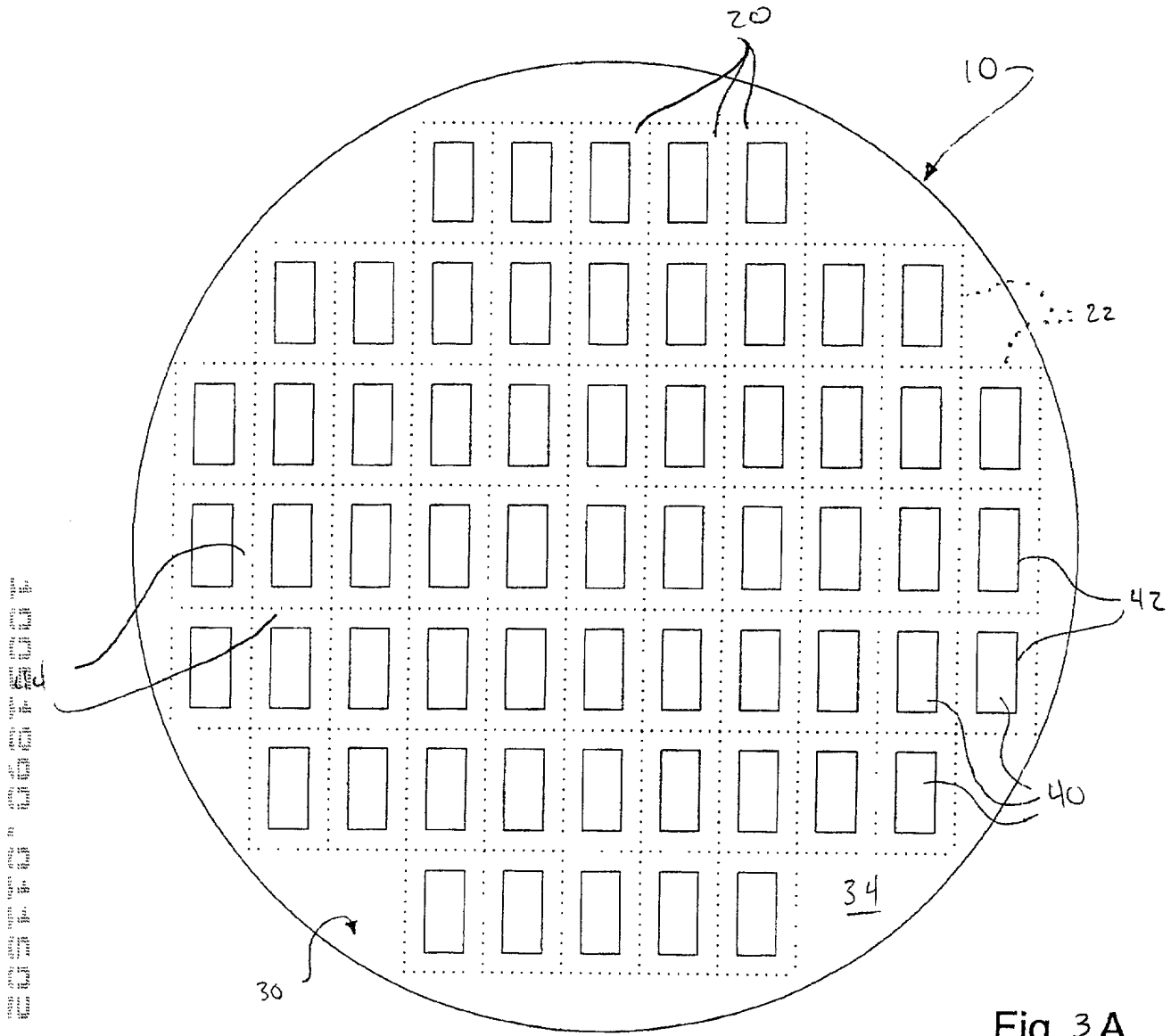


Fig. 3 A

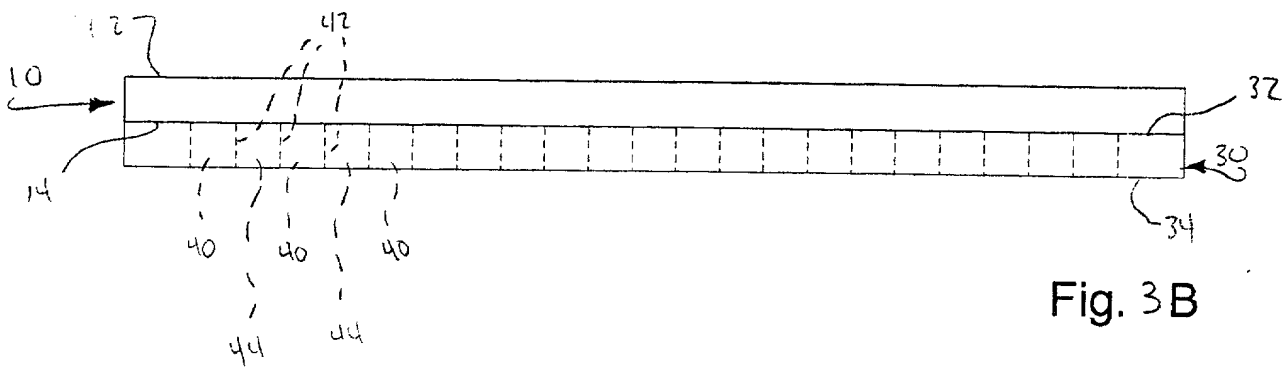


Fig. 3 B

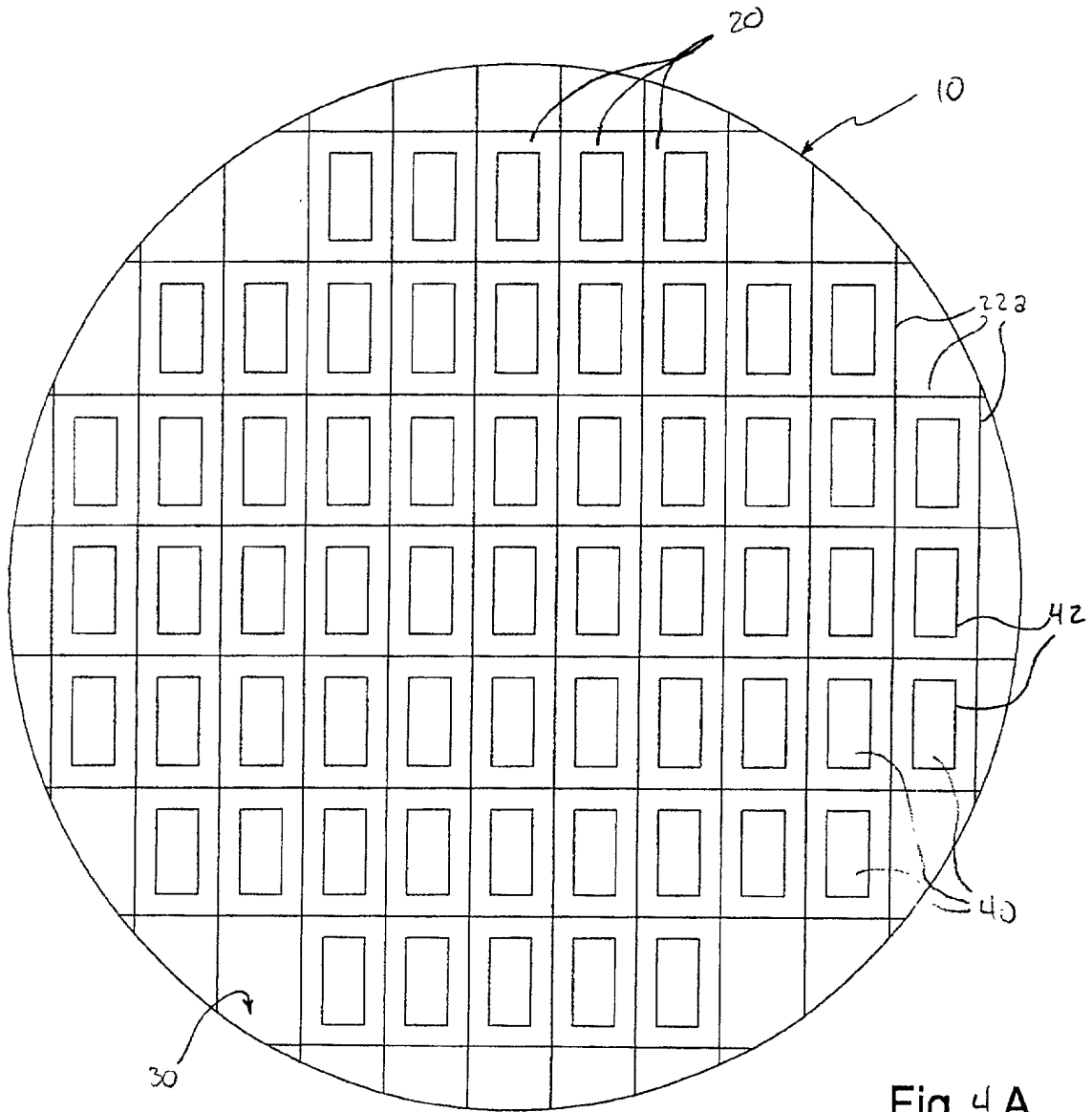


Fig. 4 A

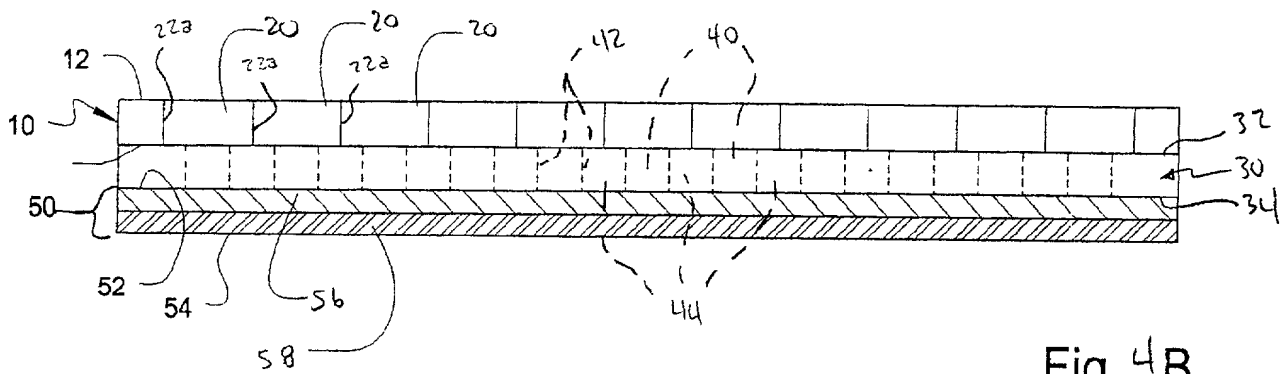


Fig. 4 B

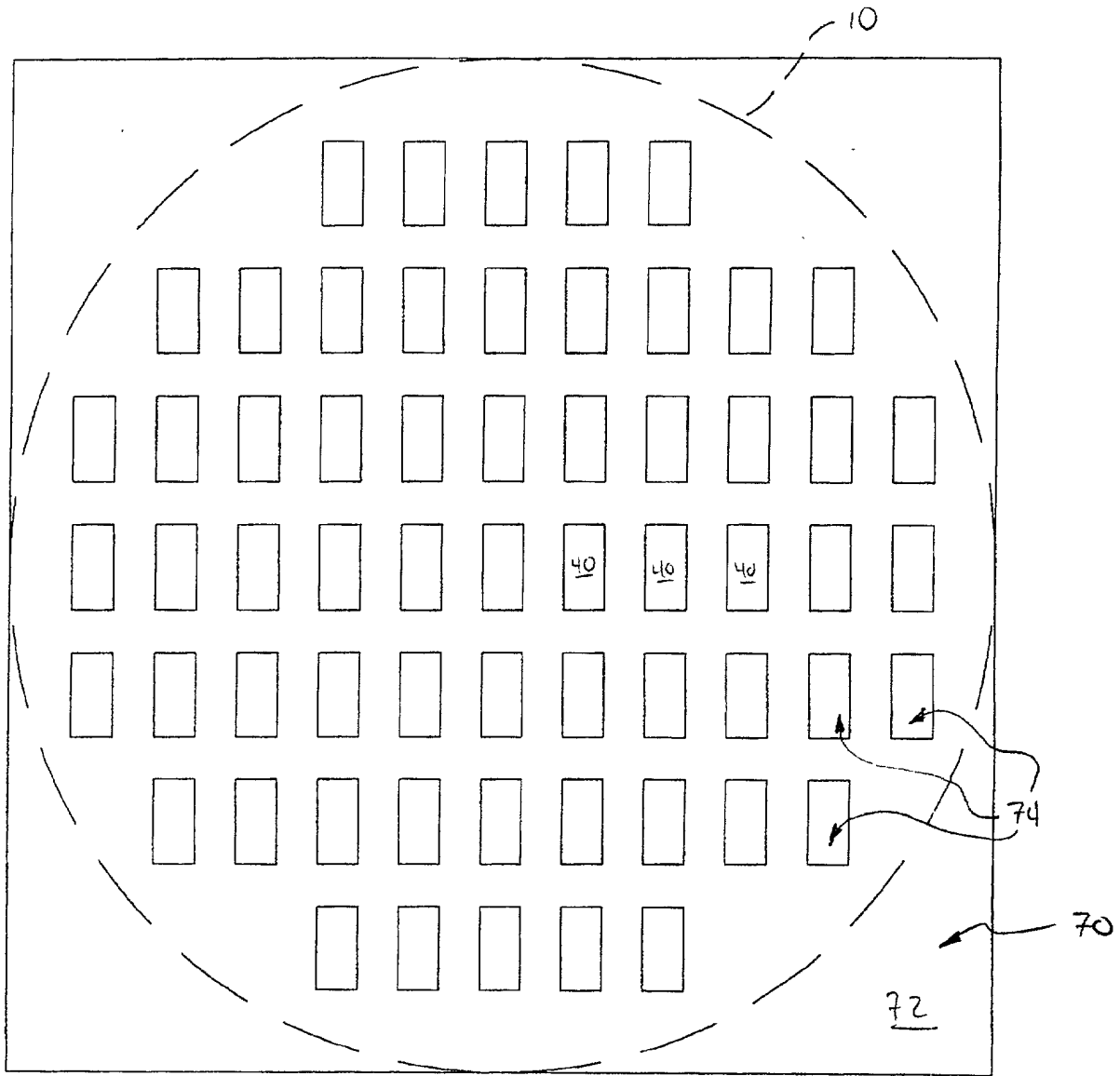


Fig. 5A

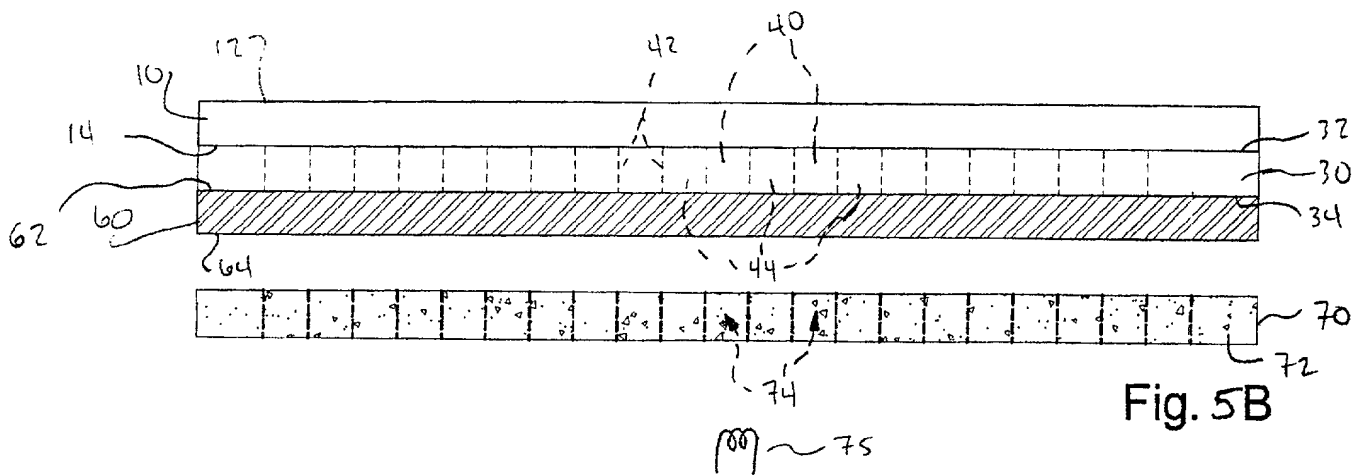


Fig. 5B

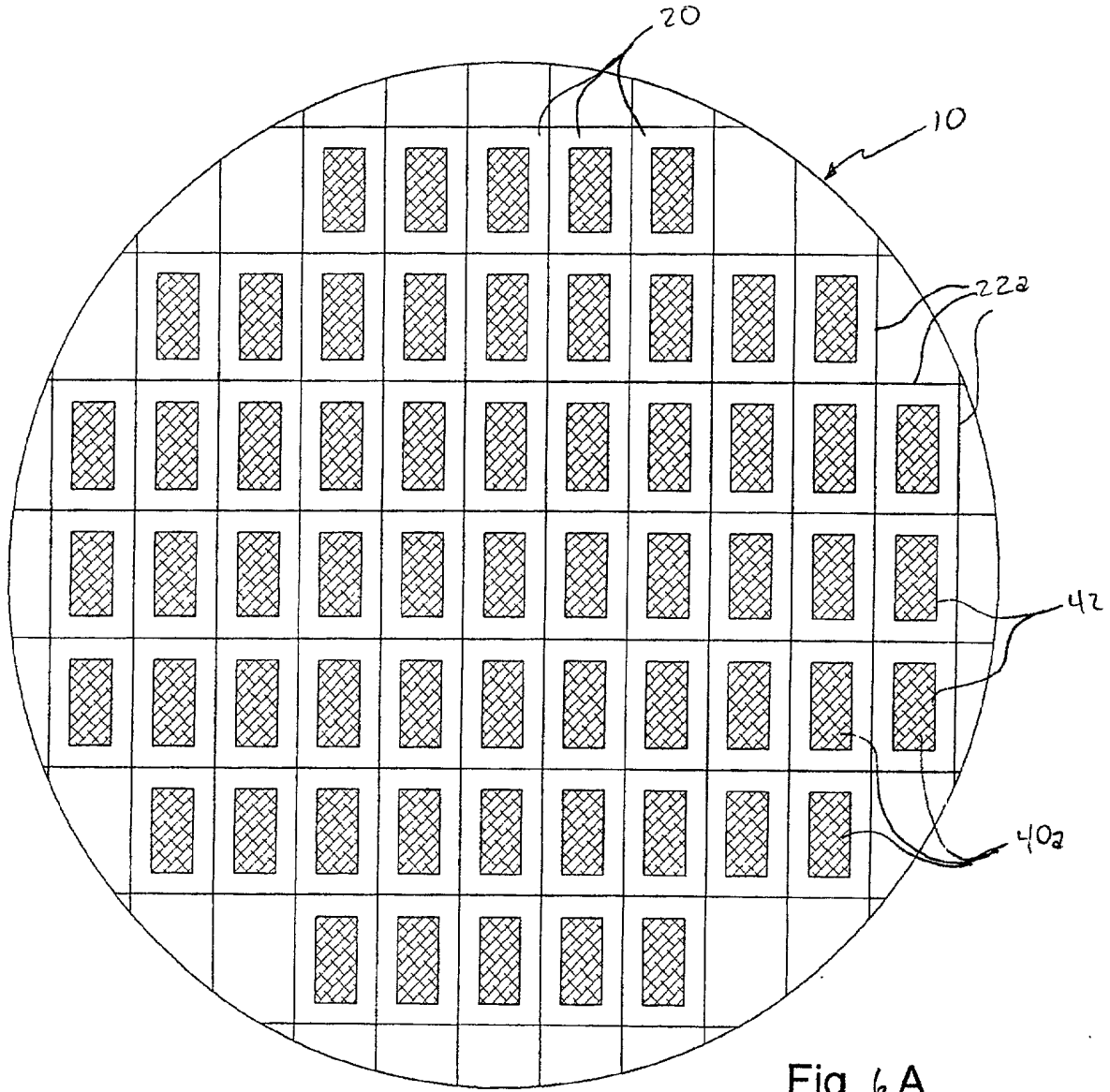


Fig. 6 A

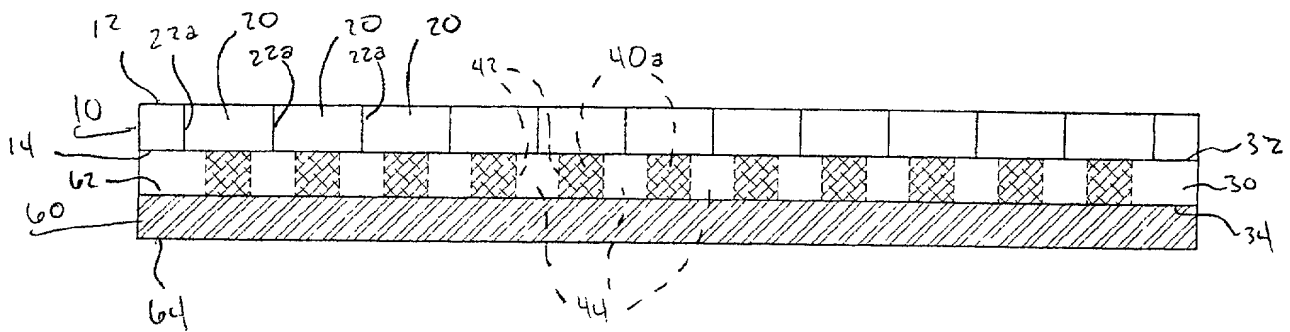


Fig. 6 B

FIG. 7 is a cross-sectional view of a device 100 in accordance with one embodiment of the present disclosure. The device 100 includes a substrate 34, a first layer 30, a second layer 20, and a third layer 10. The second layer 20 includes a plurality of cells 22a and 22b. The third layer 10 includes a plurality of cells 12 and 14. The device 100 is shown in a cross-sectional view along a dashed line 92.

